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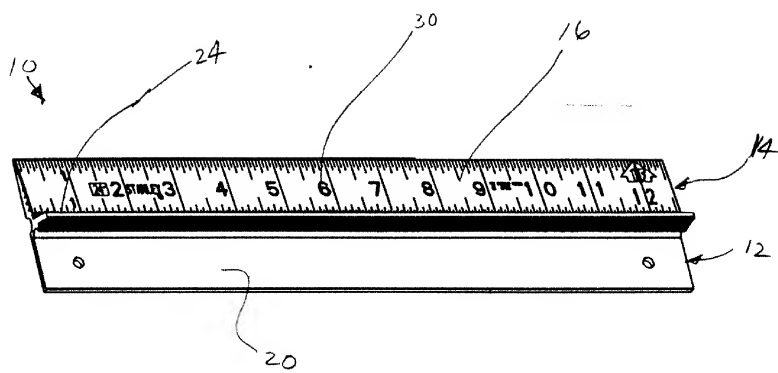


FIG. 1

FIG. 3A is a cross-sectional view of a device 10, showing a substrate 12 with a layer 14 and a layer 16. A layer 18 is disposed on top of layer 16, and a layer 20 is disposed on top of layer 18. A layer 22 is disposed on top of layer 20, and a layer 24 is disposed on top of layer 22. A layer 26 is disposed on top of layer 24, and a layer 28 is disposed on top of layer 26. A layer 30 is disposed on top of layer 28, and a layer 32 is disposed on top of layer 30. A layer 34 is disposed on top of layer 32, and a layer 36 is disposed on top of layer 34. A layer 38 is disposed on top of layer 36, and a layer 40 is disposed on top of layer 38. A layer 42 is disposed on top of layer 40, and a layer 44 is disposed on top of layer 42. A layer 46 is disposed on top of layer 44, and a layer 48 is disposed on top of layer 46. A layer 50 is disposed on top of layer 48, and a layer 52 is disposed on top of layer 50. A layer 54 is disposed on top of layer 52, and a layer 56 is disposed on top of layer 54. A layer 58 is disposed on top of layer 56, and a layer 60 is disposed on top of layer 58. A layer 62 is disposed on top of layer 60, and a layer 64 is disposed on top of layer 62. A layer 66 is disposed on top of layer 64, and a layer 68 is disposed on top of layer 66. A layer 70 is disposed on top of layer 68, and a layer 72 is disposed on top of layer 70. A layer 74 is disposed on top of layer 72, and a layer 76 is disposed on top of layer 74. A layer 78 is disposed on top of layer 76, and a layer 80 is disposed on top of layer 78. A layer 82 is disposed on top of layer 80, and a layer 84 is disposed on top of layer 82. A layer 86 is disposed on top of layer 84, and a layer 88 is disposed on top of layer 86. A layer 90 is disposed on top of layer 88, and a layer 92 is disposed on top of layer 90. A layer 94 is disposed on top of layer 92, and a layer 96 is disposed on top of layer 94. A layer 98 is disposed on top of layer 96, and a layer 100 is disposed on top of layer 98.

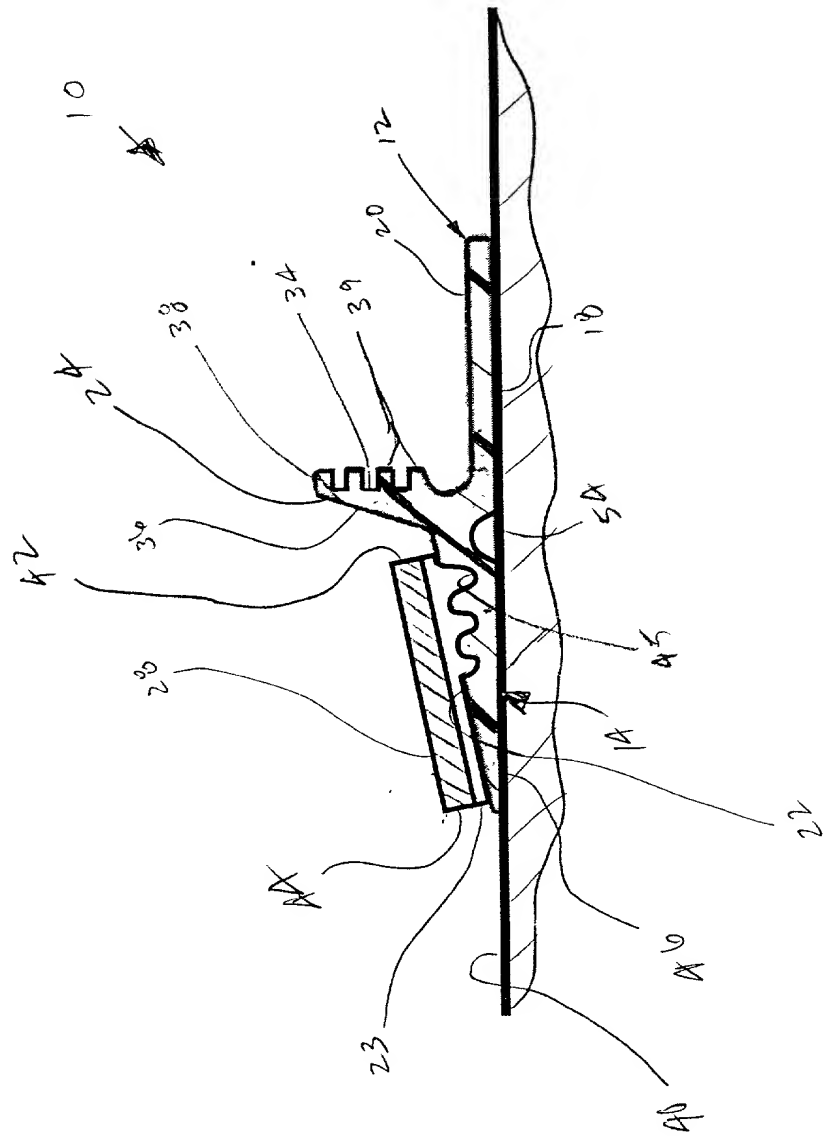
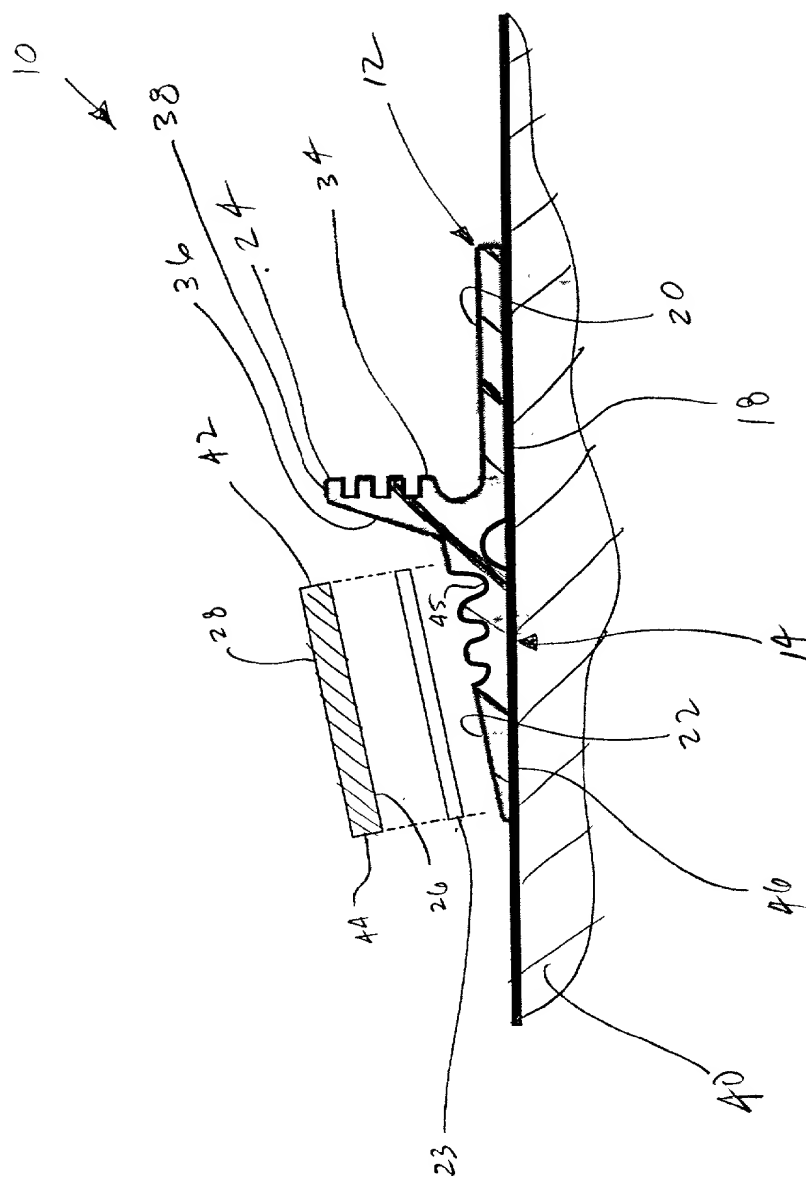


FIGURE 3A



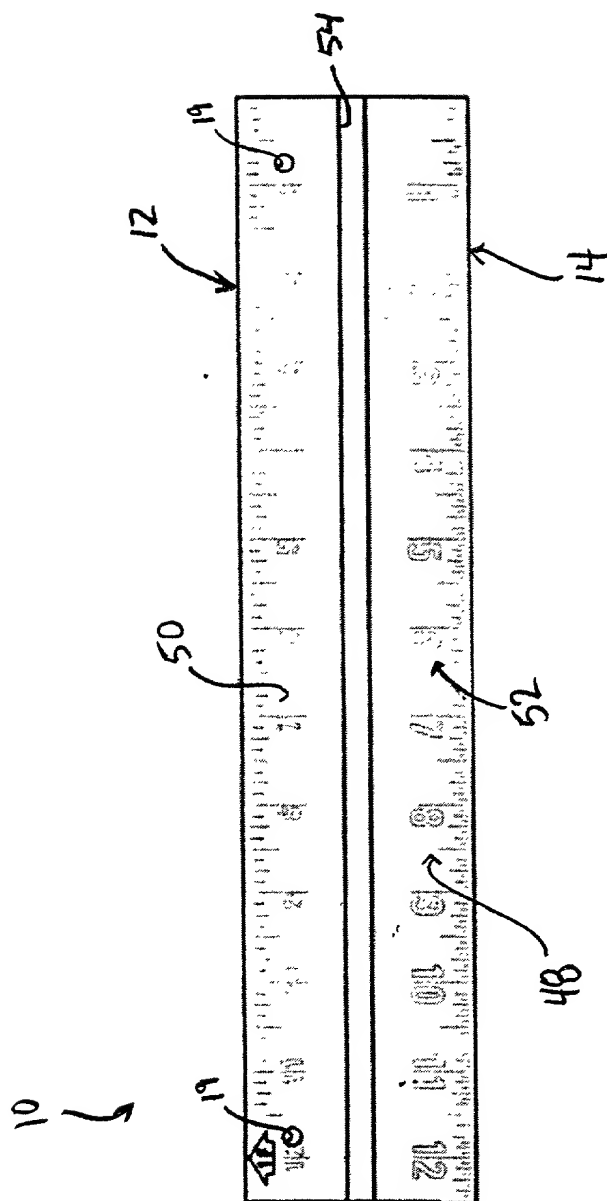


FIGURE 4

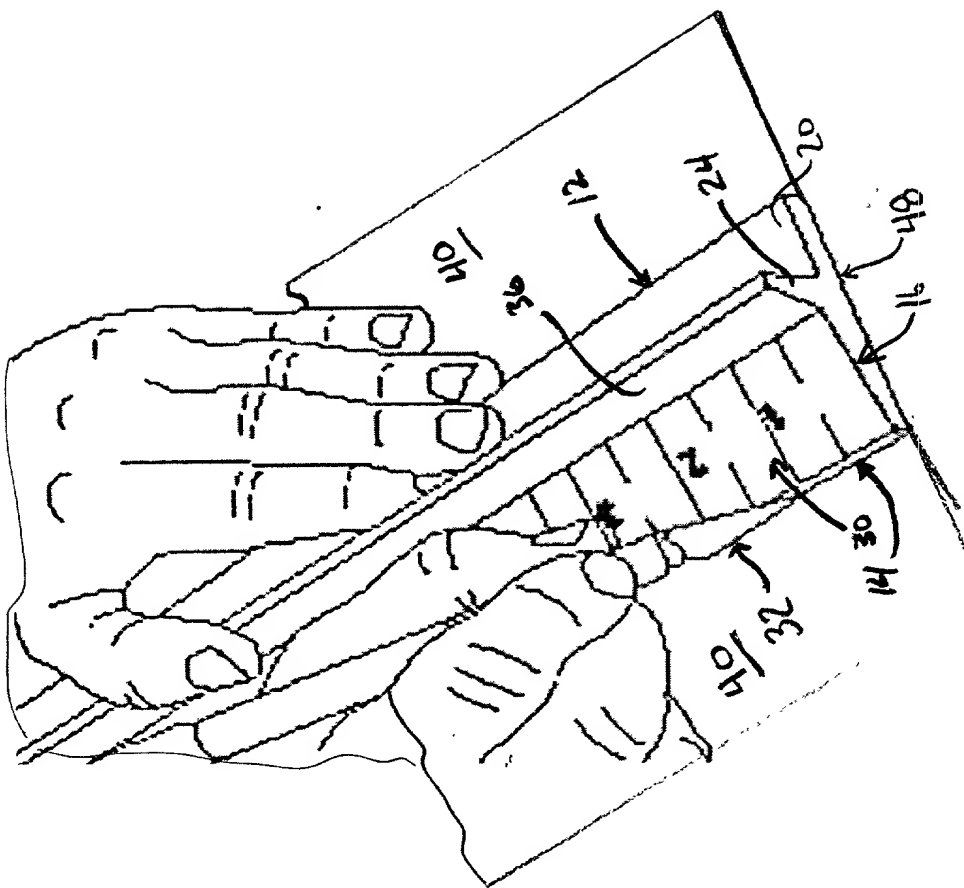


FIGURE 5

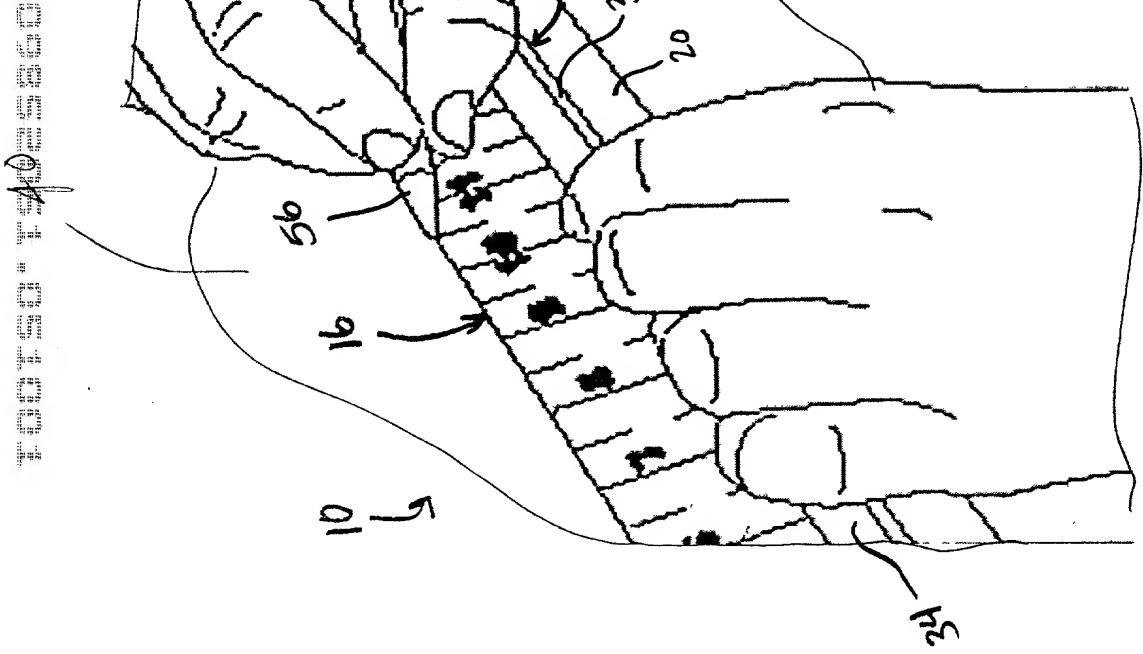


FIGURE 6

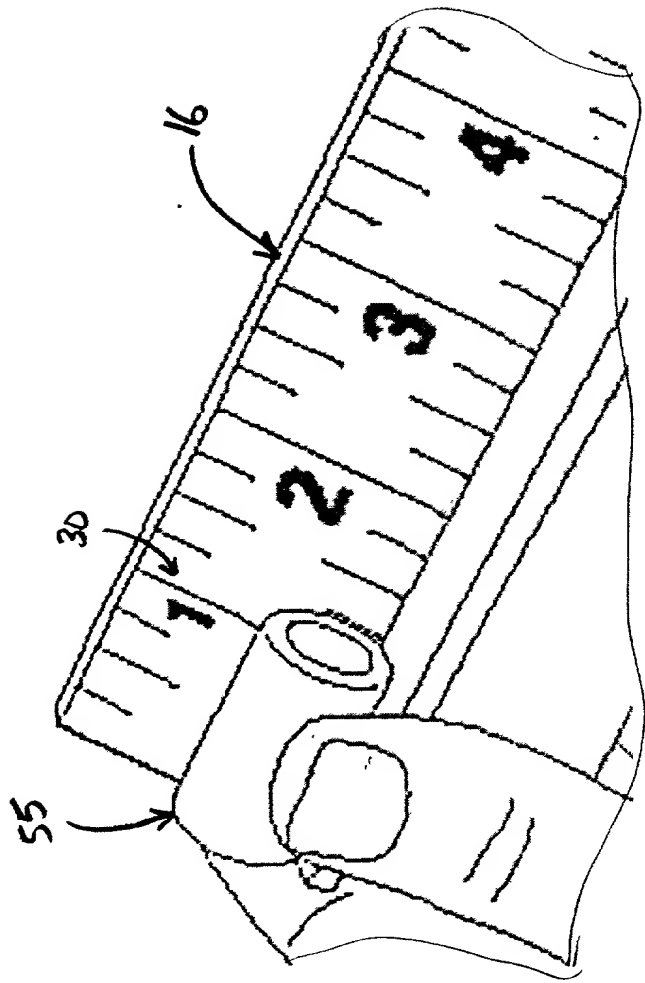


FIGURE 7

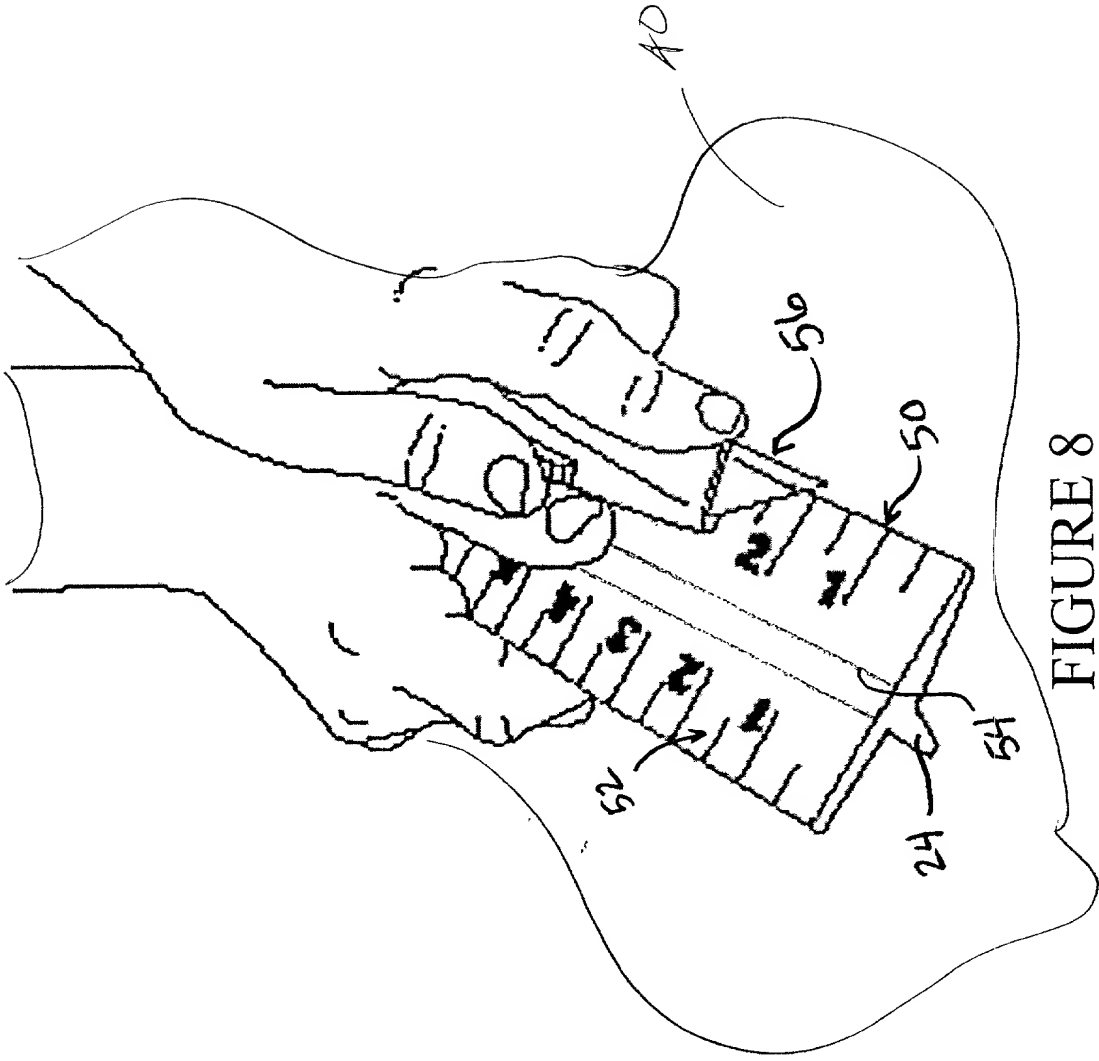


FIGURE 8

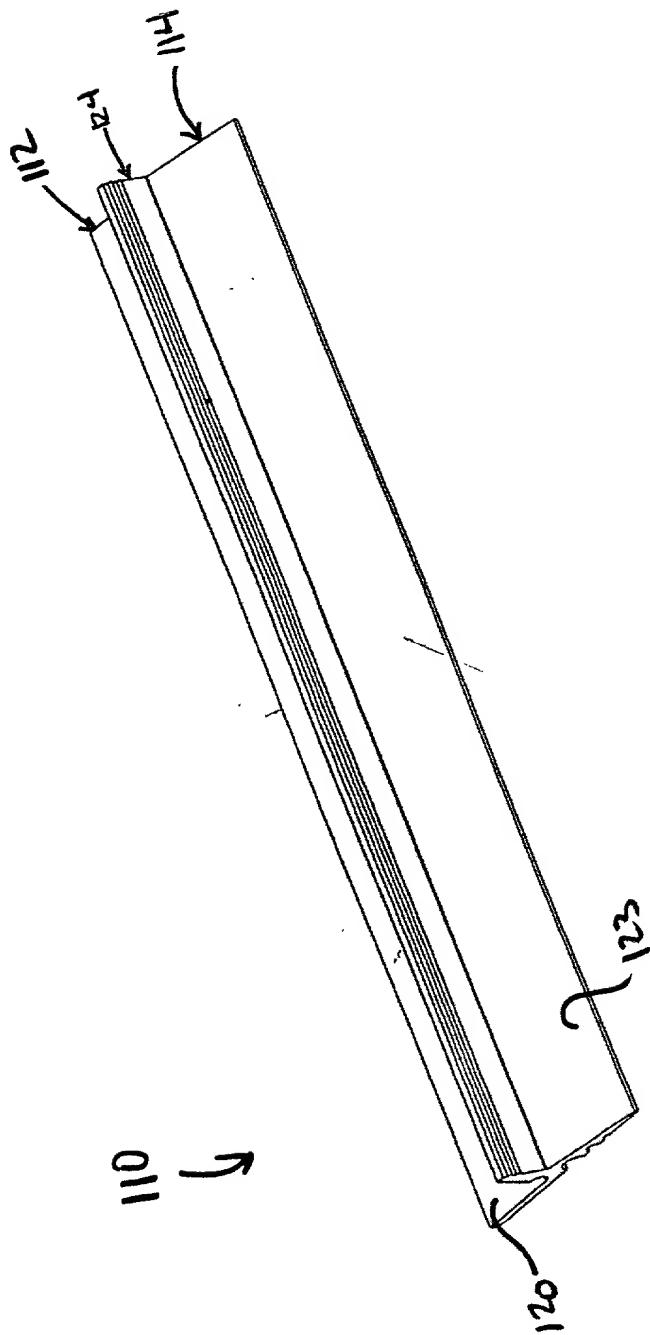


FIGURE 10

110

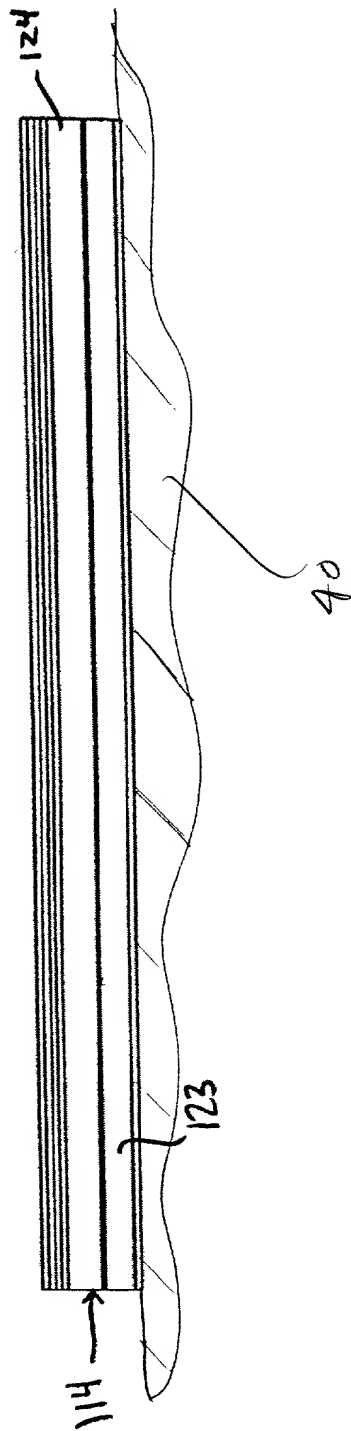


FIGURE 12

16

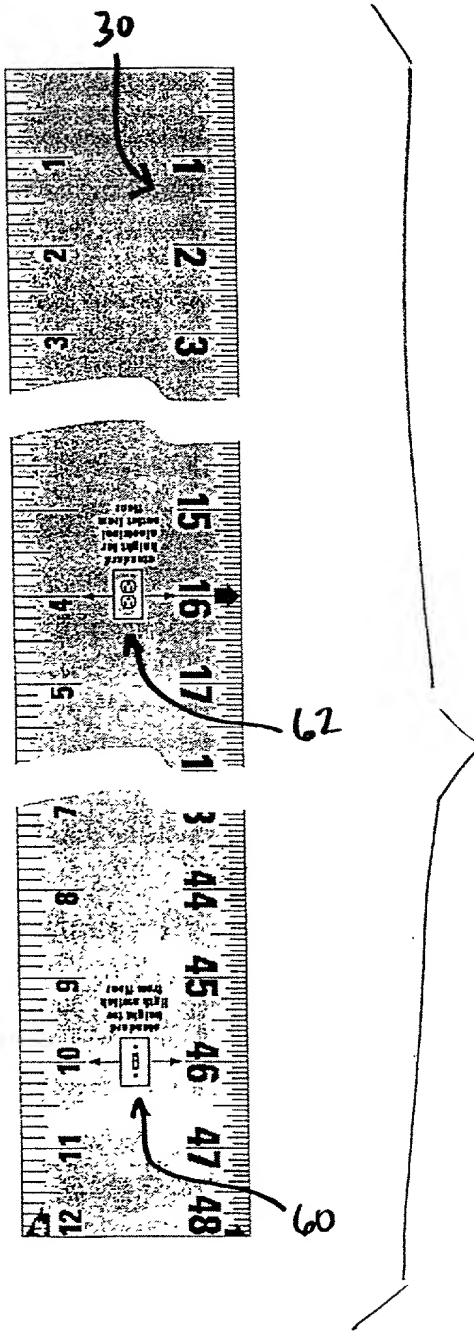


FIGURE 13

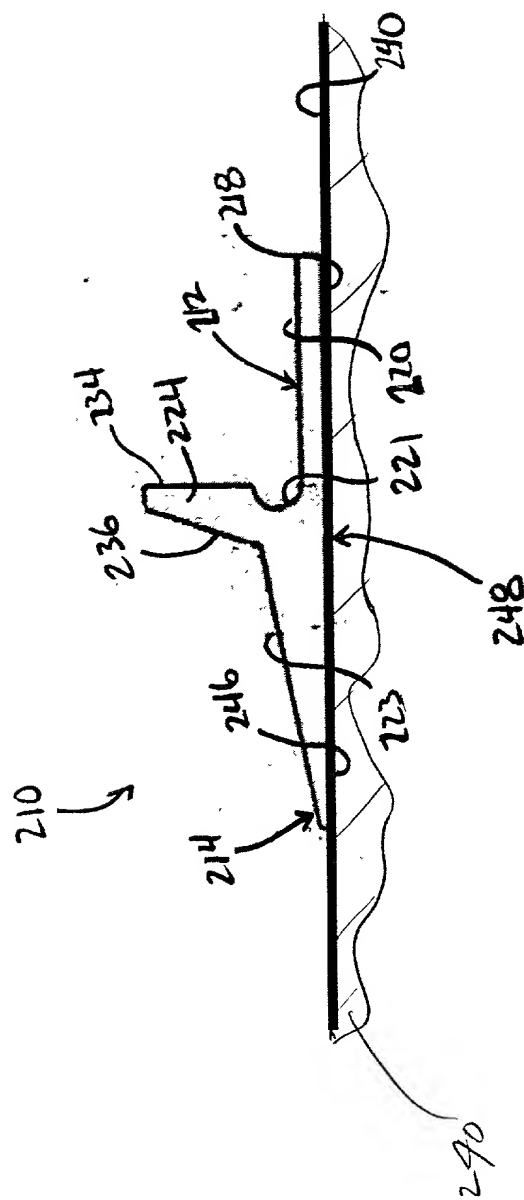


FIGURE 14

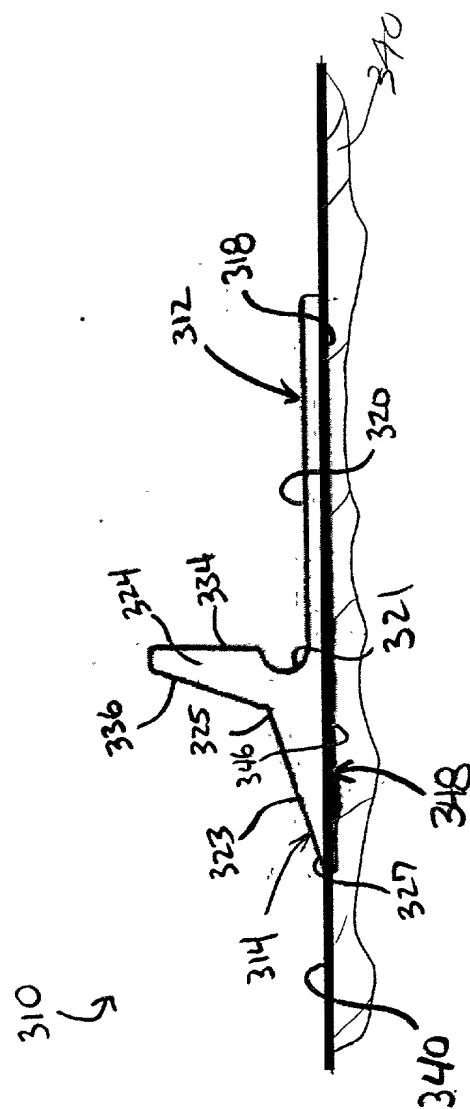


FIGURE 15

FIG. 16 is a cross-sectional view of the device 100, taken along line 16-16 of FIG. 1, showing the device 100 in a folded state. The device 100 includes a substrate 110, a gate dielectric layer 112, a gate electrode 114, a channel layer 116, a source/drain layer 118, and a passivation layer 120. The device 100 is shown in a folded state, with the substrate 110 and the gate dielectric layer 112 being folded over the channel layer 116 and the source/drain layer 118. The gate electrode 114 and the passivation layer 120 are shown on the outer surface of the folded substrate 110. The device 100 is shown in a folded state, with the substrate 110 and the gate dielectric layer 112 being folded over the channel layer 116 and the source/drain layer 118. The gate electrode 114 and the passivation layer 120 are shown on the outer surface of the folded substrate 110.

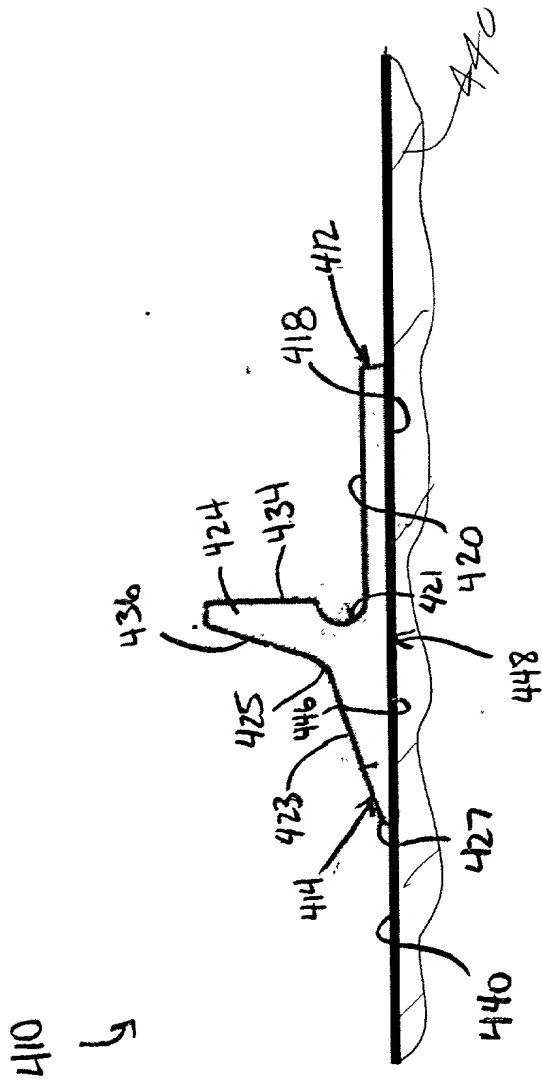


FIGURE 16

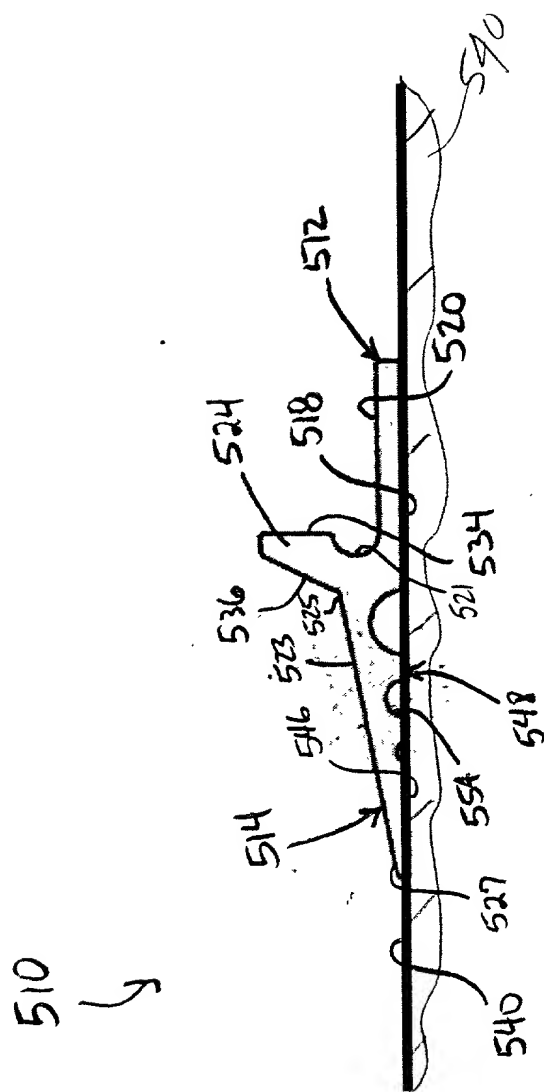


FIG. 18 is a cross-sectional view of the device of FIG. 17, taken along line 18-18 of FIG. 17, showing the device in a second state of operation. In this state, the device is in a retracted position, and the second member 610 is moved to a position adjacent to the first member 600. The second member 610 is shown in a retracted position, and the first member 600 is shown in an extended position. The device is shown in a retracted position, and the first member 600 is shown in an extended position. The device is shown in a retracted position, and the first member 600 is shown in an extended position.

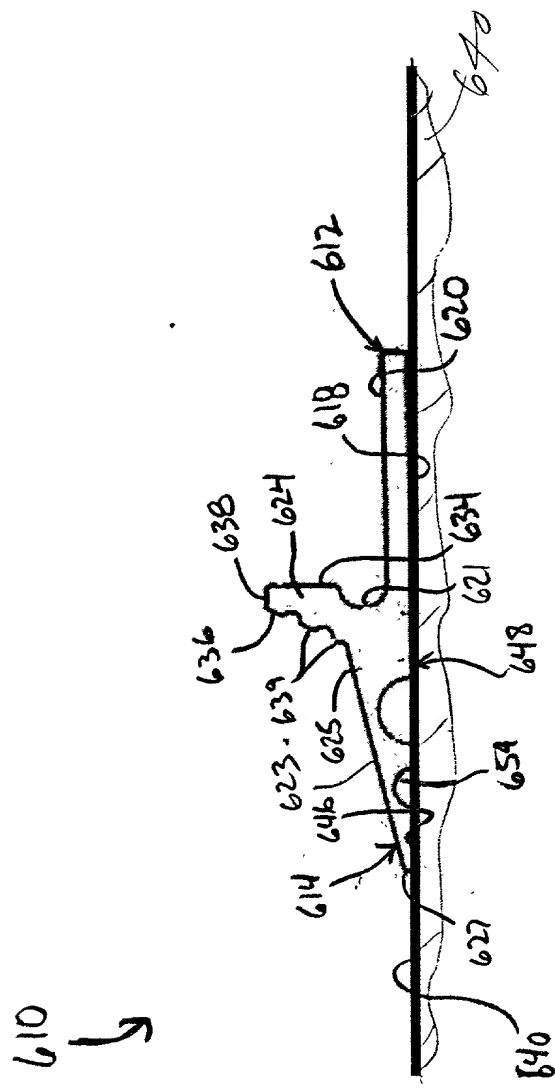


FIGURE 18

FIG. 21 is a schematic diagram of a cross-section of a device 910, showing a substrate 918, a layer 914, and a patterned layer 920. The patterned layer 920 includes features 924, 934, 936, 925, 923, 946, and 912. A dashed line 938 is also shown.

910

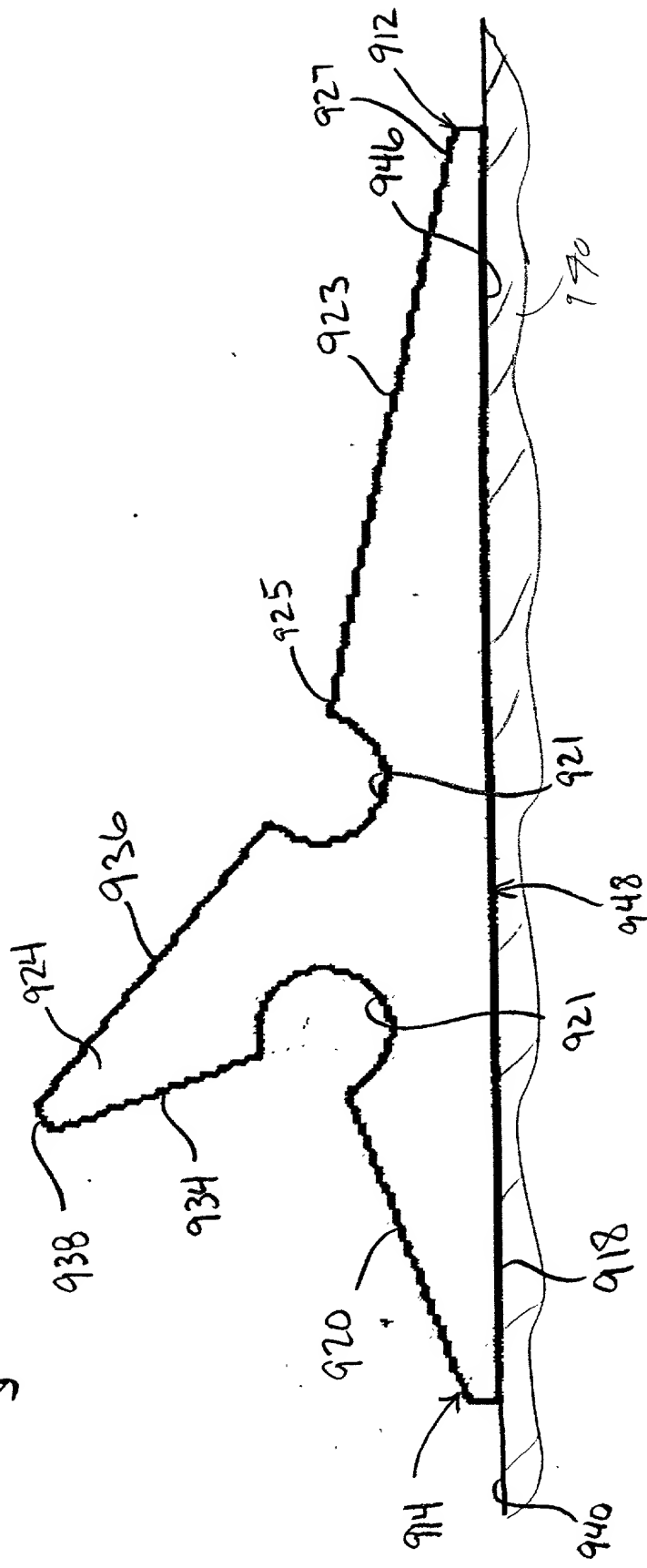


FIGURE 21

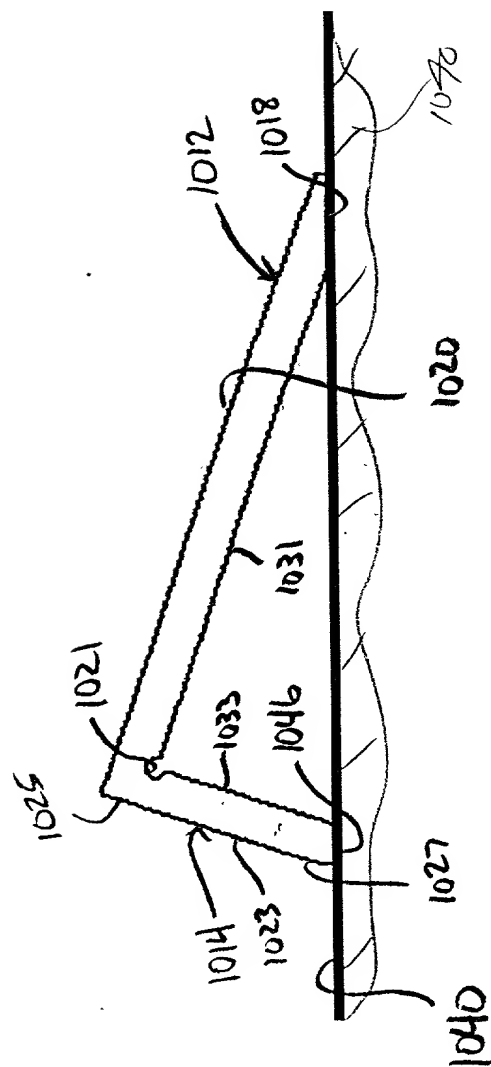


FIGURE 22

FIG. 23 is a schematic diagram of a device 1100, which may be a medical device, such as a catheter, or a surgical instrument, or a probe, or a sensor, or a transducer, or a display, or a control unit, or a power source, or a communication unit, or a storage unit, or a processing unit, or a user interface, or a combination thereof. The device 1100 includes a handle 1110, a shaft 1120, and a distal tip 1130. The handle 1110 is connected to the shaft 1120, and the shaft 1120 is connected to the distal tip 1130. The handle 1110 may include a trigger 1112, a button 1114, and a display 1116. The shaft 1120 may include a lumen 1122, a sensor 1124, and a transducer 1126. The distal tip 1130 may include a probe 1132, a sensor 1134, and a transducer 1136.

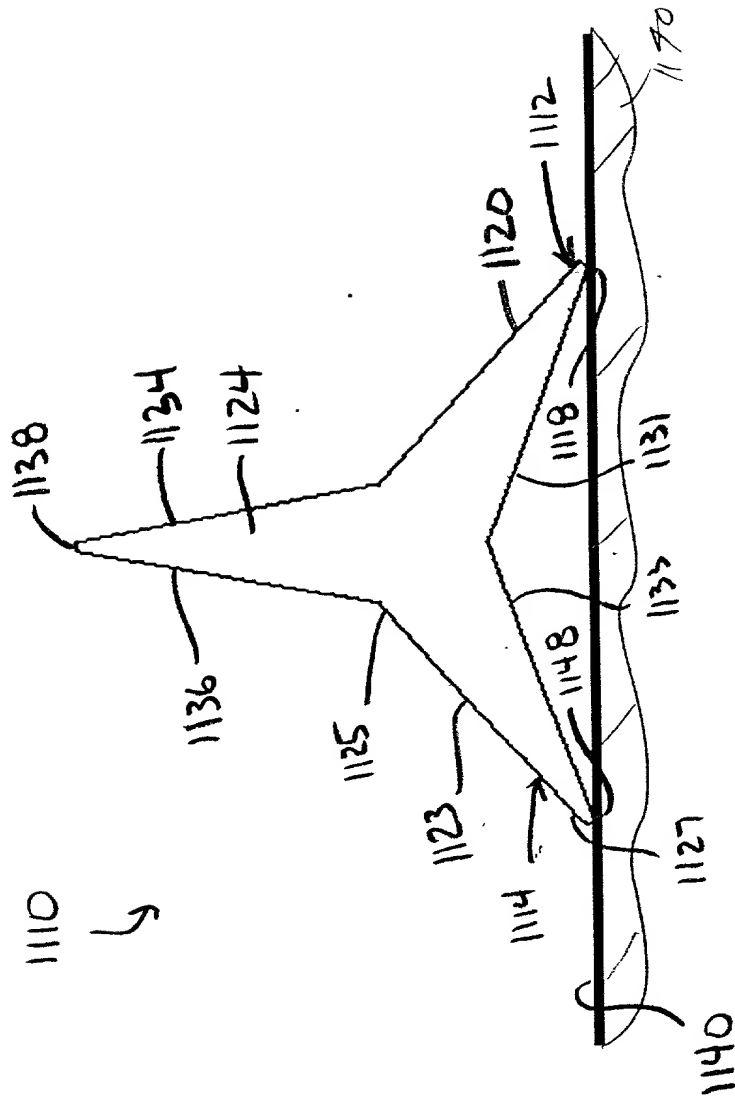


FIGURE 23

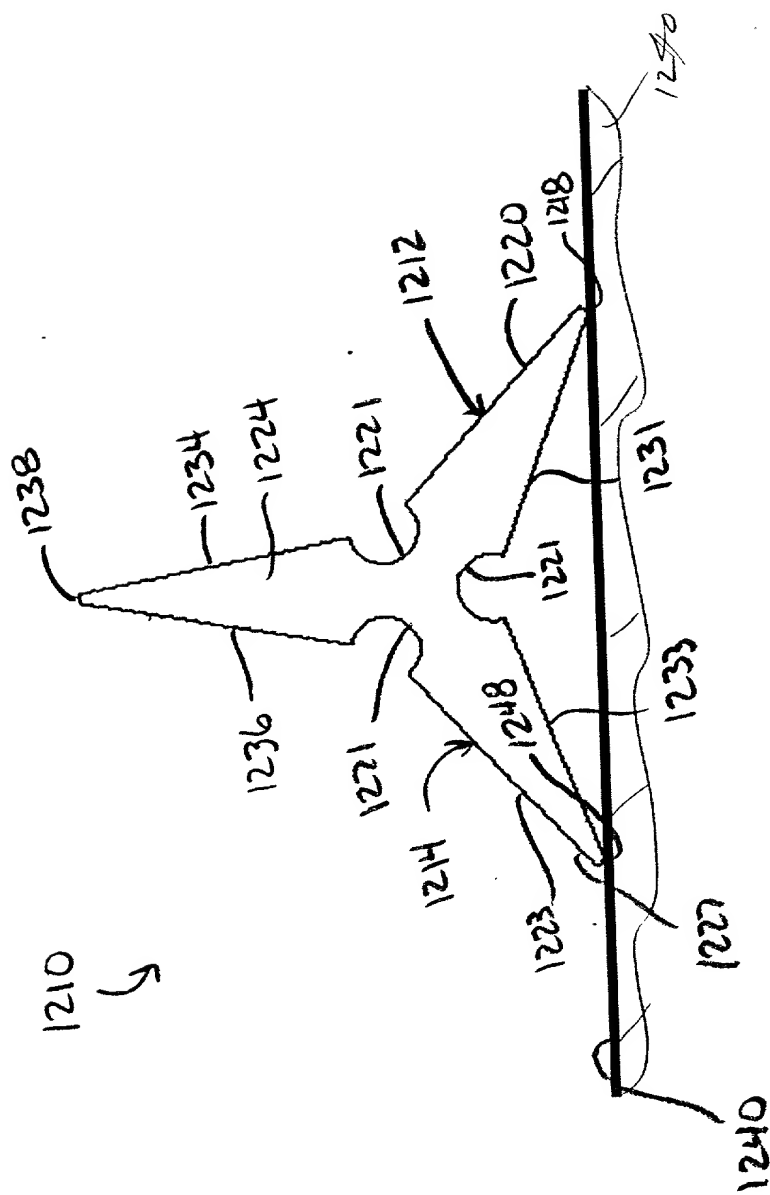


FIGURE 24

1310

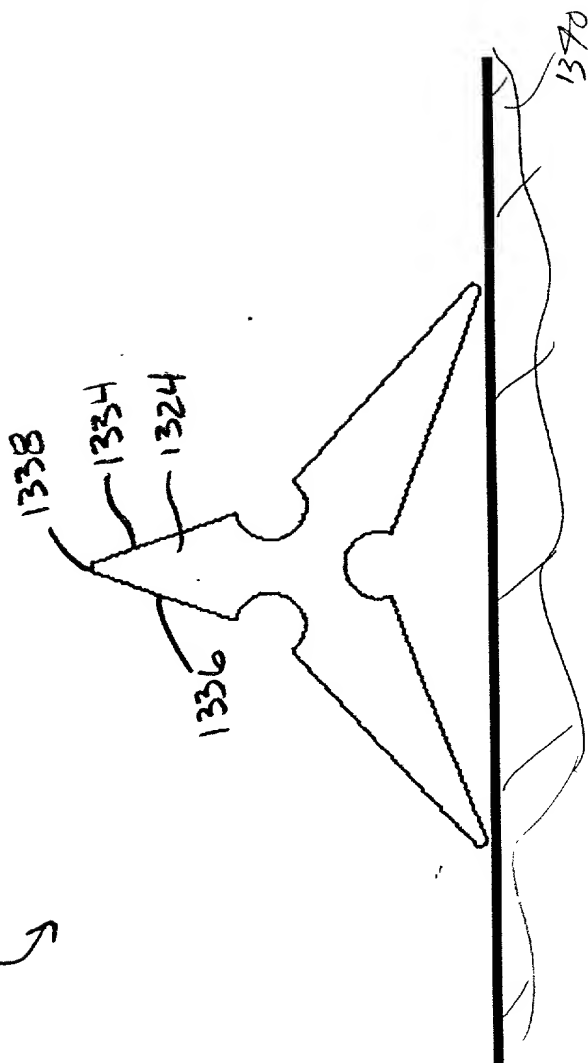


FIGURE 25



FIGURE 26

FIG. 27 is a cross-sectional view of a device 1510, showing a substrate 1540 with a layer 1543 and a structure 1538. The structure 1538 includes a central block 1524 and side blocks 1536. A layer 1545 is formed on top of the structure 1538, and a layer 1512 is formed on top of the layer 1545. A layer 1514 is formed on the substrate 1540, and a layer 1515 is formed on top of the layer 1514. A layer 1511 is formed on the substrate 1540, and a layer 1512 is formed on top of the layer 1511.

1510 ↗

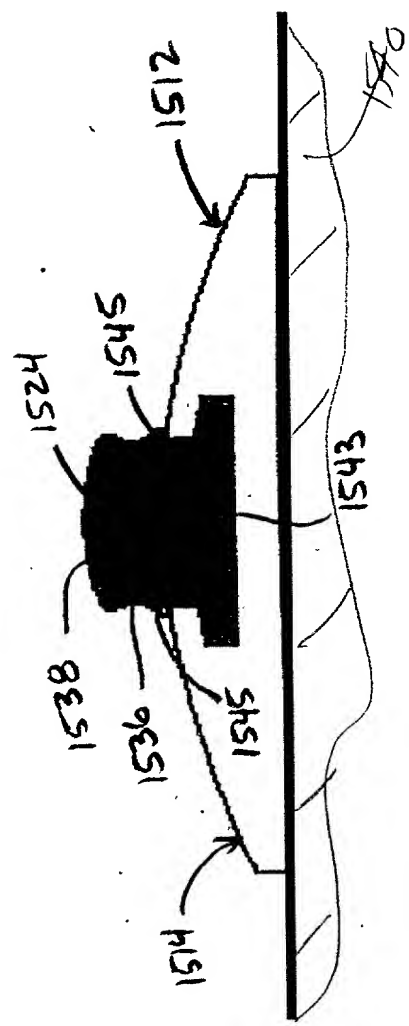


FIGURE 27